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PATENT

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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of:

Grigg et al.

Serial No.: Not yet assigned

Filed: December 28, 2001

For: LOC SEMICONDUCTOR
ASSEMBLED WITH ROOM
TEMPERATURE ADHESIVE

Examiner: Unknown

Group Art Unit: Unknown

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INFORMATION DISCLOSURE STATEMENT

Box PATENT APPLICATION
Commissioner for Patents
Washington, D.C. 20231

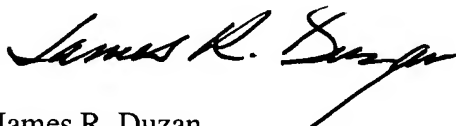
Sir:

This application is a continuation of application Serial No. 09/569,216, filed May 11, 2000, pending, which is a divisional of application Serial No. 09/176,967, filed October 22, 1998, now U.S. Patent 6,248,611 B1, issued June 19, 2001, which is a continuation of application Serial No. 08/916,977, filed August 14, 1997, now U.S. Patent 5,840,598, issued November 24, 1998.

Pursuant to M.P.E.P. 2001.06(b), the Examiner is respectfully requested to consider the information of record in the prior applications, and to confirm in the first Office Action on the merits that such art has in fact been reviewed. A PTO-1449 form listing all of the information of record in the prior applications is enclosed herewith.

This Information Disclosure Statement is filed within three (3) months of the filing date of the above-identified application, and no certification pursuant to 37 C.F.R. § 1.97(c) or a fee pursuant to 37 C.F.R. 1.17(p) is required.

Respectfully submitted,



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JRD/hlg:djp

Enclosures: Form PTO-1449
Copy of documents cited

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